IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

in re Application of

Katsunori UENO

Serial No.: 10/646,586

Group Art Unit: 2811

Filed: August 22, 2003

Examiner: Gebremariam, S.

For: SILICON CARBIDE n CHANNEL MOS SEMICONDUCTOR DEVICE AND METHOD FOR MANUFACTURING THE SAME

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RESPONSE TO ELECTION / RESTRICTION

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Sir:

In response to the Election / Restriction requirement issued March 22, 2004, applicant notes that Claims 1 - 4 which are listed in the Election/Restriction as Group I, have already been allowed in the parent application (Patent No. 6,639,273) and were canceled by the applicant in the Application Transmittal Letter filed on August 22, 2003.

In the event that claims 1-4 have been maintained in the application, applicants hereby elect Group II, claims 5 - 11, drawn to a method of forming integrated circuit device.

Respectfully submitted,

Date: 03/24/04/

Marc A. Rossi

Registration No. 31,923

Attorney Docket No. FUJI:141A